

THE FIRST TRAINING WORKSHOP ON REAL-TIME DIGITAL RADIATION MEASUREMENTS IN FPGA



University Of Michigan, Ann Arbor, MI
June 8-10, 2018

Hosted by: Consortium for Verification Technology (CVT)

Instructors: Abi Farsoni (OSU), Eric Becker (PNNL), and Angela Di Fulvio (UoM)

Register online: (ACCESS CODE: FPGA2018)

<https://www.regonline.com/realtimedigitalradiationmeasurementsinfpga>

Registration fee: students (\$150), others (\$400)

Seating is Limited. Registration will be accepted on first-come-first serve basis. Breakfast and lunch will be provided on all days. The registration also includes one dinner, on the first day. If you have any questions please email abi.farsoni@oregonstate.edu.

Highlights

- ✓ 3-day hands-on experience with real-time digital pulse processing in FPGA devices
- ✓ Application of digital filters in radiation measurements and spectroscopy
- ✓ Hardware Description Language (VHDL)
- ✓ Simulation, synthesis and implementation of FPGA designs
- ✓ Digital pulse shaping in FPGA
- ✓ Pulse-shape discrimination in FPGA
- ✓ Multi-Channel Analyzer (MCA) in FPGA
- ✓ FPGA design tests using NaI(Tl) detectors, high-speed FPGA-based DPP, and MATLAB



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